

Decade counter lab experiment using ic 7490

Decade Counter Lab Experiment Using IC 7490

Decade counter lab experiment using IC 7490 is a fundamental practical activity in digital electronics that helps students and engineers understand the operation of binary counters, frequency division, and digital counting principles. The IC 7490 is a 4-bit decade counter, which means it can count from 0 to 9 (a total of ten states) before resetting to zero. This makes it an ideal component for designing counters, digital clocks, and various counting applications. In this comprehensive guide, we will explore the working principle of IC 7490, the steps to set up a decade counter experiment, and the significance of this experiment in understanding digital logic concepts. Whether you are a student preparing for lab exams or a hobbyist interested in digital electronics, this article aims to provide a detailed and optimized overview of the decade counter lab experiment using IC 7490.

Understanding IC 7490: The Decade Counter

Overview of IC 7490

The IC 7490 is a 14-pin dual-decimal counter designed for counting applications. It is a presettable divide-by-10 counter, capable of counting from 0 to 9 in decimal and 0000 to 1001 in binary. The key features include: - 4-bit binary counter with outputs Q0, Q1, Q2, and Q3. - Divide-by-10 functionality, resetting automatically after reaching 9. - Asynchronous reset and preset inputs for easier control. - Clock input (CP) for timing control. - Carry-out (Cout) for cascading counters to extend counting range.

Block Diagram and Pin Configuration

The typical pin configuration includes: - Pin 1 (MR): Master reset (active low), resets counter to zero. - Pin 2 (PL): Parallel load (active low), loads data into the counter. - Pin 3 (CARRY OUT): Cascading output for extending counting range. - Pin 4 (CP): Clock pulse input. - Pins 5-8 (Q0-Q3): Counter outputs. - Pin 9 (GND): Ground. - Pin 10 (VCC): Power supply (+5V). - Pin 11 (P0): Parallel data input bit 0. - Pin 12 (P1): Parallel data input bit 1. - Pin 13 (P2): Parallel data input bit 2. - Pin 14 (P3): Parallel data input bit 3. Understanding the pin configuration is essential for designing and troubleshooting the experiment.

Objectives of the Decade Counter Lab Experiment

The primary objectives include: 1. Demonstrating the operation of a decade counter using IC 7490. 2. Understanding the concept of counting, reset, and preset functions. 3. Cascading multiple ICs for extended counting. 4. Observing the output waveforms and frequency division. 5.

Implementing applications such as digital clocks or event counters.

Components Required for the Experiment

To set up the decade counter experiment using IC 7490, the following components are necessary: - IC 7490 (Decade Counter) - Power supply (5V DC) - Push-button switches for clock pulse input - Connecting wires - Breadboard or PCB - LEDs (to display outputs Q0-Q3) - Resistors (220Ω or 470Ω for current limiting on LEDs) - Debouncing circuit (optional, for cleaner clock pulses) - Cables for cascading multiple counters (if extending count beyond 10)

Step-by-Step Procedure for the Experiment

1. Circuit Connection

- Power the IC by connecting VCC (Pin 10) to +5V and GND (Pin 9) to ground. - Connect the clock pulse source to the CP (Pin 4). - Connect LEDs to Q0-Q3 outputs through current-limiting resistors. - Connect the reset (MR) pin to a switch or control circuit to reset the counter when needed. - Connect parallel load pins (PL) if presetting the counter. - For cascading, connect the CARRY OUT (Pin 3) of one IC to the clock input (CP) of the next IC.

2. Applying the Clock Pulses

- Use a push-button or a square wave generator to provide clock pulses. -

Press the button repeatedly to observe the counter advancing from 0 to 9.

- For continuous operation, connect the clock to an oscillator circuit.

3. Observing the Outputs

- Monitor the LEDs connected to Q0-Q3. - The binary count will be displayed visually on the LEDs. - When the count reaches 9, the IC should automatically reset to 0, demonstrating the decade operation.

4. Verifying the Counter Functionality

- Use a frequency counter or oscilloscope to verify the counting frequency.
- Check the reset operation by manually triggering the reset pin. -

Observe the carry-out pulse at the 9 count, which can be used to cascade with other counters.

Understanding the Working of IC 7490 in the Experiment

Counting Sequence

The IC 7490 counts in decimal from 0 to 9. The binary outputs (Q0-Q3) represent the count in binary form. The IC uses asynchronous reset to return the count to zero after reaching 9, ensuring the counter operates as a decade counter.

Cascading Counters

To extend the counting range beyond 9, multiple ICs can be cascaded. The CARRY OUT of the first IC triggers the clock of the second IC,

enabling complex counting sequences like decimal counting up to 99, 999, etc.

Frequency Division

The IC acts as a frequency divider, halving the frequency of the clock pulse at each decade. This feature is crucial in digital clock circuits where precise time division is required.

Applications of Decade Counters Using IC 7490

- Digital clocks and timers - Event counters - Frequency dividers in communication systems - Digital voltmeters and multimeters - Timing circuits in industrial automation - Frequency synthesizers

Tips and Troubleshooting

- Ensure proper power supply (5V DC) connections. - Check for correct wiring of outputs and clock input. - Use debouncing circuitry for manual clock pulses to avoid erratic counting. - Confirm that the reset and preset pins are correctly connected to control the counter operation. - Use a logic analyzer or oscilloscope to verify signal integrity.

Conclusion

The decade counter lab experiment using IC 7490 is an essential exercise for understanding the fundamental principles of digital counting, frequency division, and cascading digital counters. By assembling and testing the circuit, students gain practical insights into how digital systems

perform counting operations, which are the backbone of more complex digital electronics applications. Mastery of this experiment lays a strong foundation for further exploration into counters, timers, and digital clock design. In summary, the IC 7490 provides a simple yet powerful platform for implementing decade counters, making it an invaluable component in both educational and practical digital electronics projects. Through careful setup, observation, and analysis, learners can deepen their understanding of digital logic and prepare for advanced digital system design.

Decade Counter Lab Experiment Using IC 7490 The Decade Counter Lab Experiment Using IC 7490 is a fundamental yet essential exercise in digital electronics that provides insight into binary counting, decade counting, and the practical application of Johnson and ripple counters. This experiment is particularly significant for students and engineers aiming to understand the working principles of counters, their configurations, and their real-world applications in digital systems such as frequency dividers, digital clocks, and event counters.

Introduction to IC 7490 and Decade Counters

What is the IC 7490?

The IC 7490 is a 4-bit binary ripple counter manufactured by most semiconductor manufacturers. It is a part of the 7400 series of TTL (Transistor-Transistor Logic) integrated circuits and is widely used for counting applications. The IC contains two separate 4-bit decade counters, which are capable of counting from 0 to 9 (decimal), making it ideal for applications requiring decimal counting rather than binary. Key

Features of IC 7490: - 4-stage decade counter - BCD (Binary-Coded Decimal) output capability - Count from 0 to 9, then reset automatically - Compatible with TTL logic levels - Push-button reset feature - Count enable inputs for controlling counting operations - Power supply voltage typically 5V to 15V

Understanding Decade Counters

A decade counter is a type of digital counter that counts from 0 to 9 (decimal) and then resets to 0 automatically, thus completing one cycle every ten counts. These are crucial in applications where counting in decimal is necessary, such as digital clocks, odometers, and event counters. Characteristics of Decade Counters: - Count sequence from 0 to 9 (0000 to 1001 in binary) - Reset after reaching 9 to start again at 0 - Can be configured as ripple or synchronous counters - Useful in dividing frequency by 10

Components and Circuit Requirements

Primary Components

To perform the decade counter experiment with IC 7490, the following components are essential: - IC 7490 (Decade Counter) - Power supply (5V DC) - Connecting wires - Breadboard or PCB - Push-button switches for counting and reset - Resistors (for pull-up or current limiting, if necessary) - LED indicators (to visualize outputs) - Clock pulse generator (using an astable multivibrator or 555 timer IC)

Additional Equipment

- Digital multimeter (for voltage measurement) - Logic probe (for verifying logic states) - Oscilloscope (for waveform visualization, optional)

Working Principle of IC 7490 in Decade Counting

The IC 7490 contains two independent 4-bit binary ripple counters, which are cascaded to implement a decade counter. The core working involves counting input pulses and advancing the count in binary form. When the count reaches 9 (1001 binary), the circuit resets automatically to 0 (0000), thus completing a decimal cycle. Operational Highlights: - The device counts on each clock pulse received at the clock input. - The count progresses through binary states (0000 to 1001). - At the count of 9, the IC generates a reset signal, clearing the flip-flops and starting the count anew. - The outputs Q0, Q1, Q2, Q3 represent the binary count. Key Pins and Their Functions: | Pin Number | Function | Description | |||| | 1, 2, 6, 7 | Outputs Q0 to Q3 | Binary count outputs | | 3, 4, 8, 9 | Reset pins | Resets the counter when activated | | 10, 11 | Enable pins | Enable counting when active | | 14 | Clock input | Receives clock pulses | | 16 | Vcc | Power supply (+5V) | | 7 | GND | Ground | Counting Sequence: | Count | Binary | Q3 Q2 Q1 Q0 | Reset? | |||| | 0 | 0000 | 0 0 0 0 | No | | 1 | 0001 | 0 0 0 1 | No | | 2 | 0010 | 0 0 1 0 | No | | 3 | 0011 | 0 0 1 1 | No | | 4 | 0100 | 0 1 0 0 | No | | 5 | 0101 | 0 1 0 1 | No | | 6 | 0110 | 0 1 1 0 | No | | 7 | 0111 | 0 1 1 1 | No | | 8 | 1000 | 1 0 0 0 | No | | 9 | 1001 | 1 0 0 1 | Yes (Reset triggered) | When the counter reaches 1001, the reset pins are activated to clear the flip-flops, bringing the output back to 0000, thereby completing the cycle.

Step-by-Step Procedure for the Experiment

1. Circuit Assembly

- Connect the IC 7490 on the breadboard. - Connect power supply pins (Vcc to +5V, GND to ground). - Connect the clock pulse generator to the clock input pin (pin 14). - Connect output pins (Q0, Q1, Q2, Q3) to LEDs via current-limiting resistors to visualize counting. - Connect reset pins (pin 4 and/or 3) through push-buttons for manual reset, if needed. - Connect enable pins (pin 10 and 11) to logic high (Vcc) for normal operation.

2. Applying Clock Pulses

- Use a clock pulse generator such as a 555 timer configured in astable mode or a manual push-button to provide pulses. - Ensure the pulse frequency is suitable for observing the counting sequence comfortably (e.g., 1Hz to 2Hz).

3. Monitoring and Recording

- Observe the LEDs corresponding to the output pins as they change states with each clock pulse. - Record the binary count and corresponding decimal value at each step.

4. Testing Reset Functionality

- Manually press the reset push-button at any count to verify the counter resets to zero. - Observe the LEDs returning to the 0 (all off) state.

5. Verifying the Count Range

- Continue clock pulses until the counter reaches 9 (1001). - Confirm that after count 9, the counter resets to 0 automatically, demonstrating the decade counting feature.

Data Observation and Analysis

- Count Sequence: Ensure the sequence follows the correct binary pattern from 0000 to 1001. - LED Indicators: The LEDs should turn on and off in sync with the binary output, providing visual confirmation of each count. - Reset Functionality: When reset is activated, all outputs should immediately go to zero. - Frequency Division: If using a clock of a certain frequency, check the output frequency at Q3 or Q2 to verify division by 10.

Applications of Decade Counters and IC 7490

Decade counters are fundamental in various digital systems. Some key applications include: - Digital Clocks: Counting seconds, minutes, or hours. - Frequency Dividers: Dividing high-frequency signals for measurement or control purposes. - Event Counters: Counting objects, pulses, or occurrences in industrial systems. - Odometers and Digital Meters: Counting units in measurement devices. - Binary to Decimal Conversion: Simplifying binary counting for human-readable displays. IC 7490 specifically is suitable for building: - Decimal counters with clear reset functionality. - Modular counters in combination with other digital ICs for complex counting sequences.

Advantages and Limitations of IC 7490 in Decade Counting

Advantages: - Simple to connect and operate. - Provides clear binary outputs for easy interpretation. - Built-in reset and enable features streamline design. - Low power consumption suitable for portable devices. - Widely available and cost-effective. Limitations: - Ripple counter design introduces propagation delay, which can limit high-speed applications. - Requires careful debouncing in manual reset or clock inputs. - Limited to TTL logic levels, which may require level shifting in modern systems. - Not suitable for high-frequency counting without modifications or additional buffering.

Extensions and Advanced Topics

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Questions & Answers About decade

counter lab experiment using ic 7490

No	Question	Answer
1	What is the primary function of the IC 7490 in a decade counter lab experiment?	The IC 7490 is a decade counter IC used to count from 0 to 9 (decades) in binary form, making it essential for designing digital counters and understanding counting sequences in lab experiments.
2	How do you connect the IC 7490 to ensure proper counting in the experiment?	The IC 7490 should be connected with appropriate clock pulses to the clock input, with reset and enable pins configured as per the required counting sequence, and output pins used to display the binary count for observation.
3	What are common troubleshooting steps if the decade counter isn't counting correctly in the lab?	Common troubleshooting includes checking power supply connections, verifying the integrity of the clock pulse source, ensuring reset pins are correctly configured, and confirming that output connections are properly wired without shorts or open circuits.
4	How can the outputs of the IC 7490 be used to display decimal numbers in the experiment?	The outputs Q0 to Q3 of the IC 7490 represent the binary count, which can be connected to a 7-segment display or other display modules to visually show the current decimal count from 0 to 9.
5	What are some practical applications of a decade counter using IC 7490 demonstrated in the lab?	Practical applications include digital clocks, frequency dividers, event counters, and timing circuits, all of which can be demonstrated using a decade counter in the lab to understand their digital counting principles.

decade counter, IC 7490, digital counter, binary counter, ripple counter,

decade counter circuit, 7-segment display, counter experiment, digital electronics lab, IC 7490 pin configuration

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